AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

- 1. (Canceled)
- (Currently Amended) An image processor comprising:
 an output device which processes input data and outputs the processed data;

in the input data, in parallel to the data processing by said output device; and

a stop controller which makes said output device stop to output the

processed data at an irregular timing after said detector detects the specified pattern;

and

The image processor according to claim 1, further comprising a timing generator which generates a random timing, wherein said stop controller makes said output device stop to output the processed data at the timing generated by said timing generator after said detector detects the specified pattern.

3. (Currently Amended) The image processor according to claim $4 \underline{2}$, further comprising:

a memory device which stores the input data or the processed data; and

a data deleter which deletes data stored in said memory device when the specified pattern is detected.

- 4. (Currently Amended) The image processor according to claim $4 \ \underline{2}$, wherein said output device outputs the processed data to a printer which prints an image on a sheet of paper.
- 5. (Original) The image processor according to claim 4, further comprising a transmitter which transmits a reset signal to clear data stored in a buffer in the printer when the specified pattern is detected by said detector.
- 6. (Currently Amended) An image processor comprising:

 an output device which processes input data and outputs the processed data;

a detector which detects whether data of a specified pattern is included in the input data or in the processed data, in parallel to the data processing by said output device; and

a stop controller which makes said output device stop to output the processed data at an irregular timing after said detector detects the specified pattern; and

The image processor according to claim 4, further comprising a transmitter which transmits a command signal to the printer for feeding a sheet of paper reversely on which an image is formed based on the processed data when the specified pattern is detected by said detector;

wherein said output device outputs the processed data to a printer which prints an image on a sheet of paper.

- 7. (Canceled)
- 8. (Currently Amended) A print system including an image processor and a printer which prints data received from said image processor;

wherein said image processor comprises:

an output device which processes input data and outputs the processed data;

in the input data, in parallel to the data processing by said output device; and

a stop controller which makes said output device stop to output the

processed data at an irregular timing after said detector detects the specified pattern;

wherein said printer prints an image on a sheet of paper, based on the data outputted by said output device; and

The print system according to claim 7, said image processor further comprising a timing generator which generates a random timing, wherein said stop controller makes said output device stop to output the processed data at the timing generated by said timing generator after said detector detects the specified pattern.

9. (Currently Amended) The print system according to claim 7 8, said image processor further comprising a transmitter which transmits a reset signal for clearing data stored in a buffer in the printer when the specified pattern is detected

by said detector, said printer further comprising a clear device which clears data stored in a buffer in said printer in response to the reset signal received from said transmitter.

10. (Currently Amended) A print system including an image processor and a printer which prints data received from said image processor;

wherein said image processor comprises:

an output device which processes input data and outputs the processed data;

in the input data, in parallel to the data processing by said output device; and

a stop controller which makes said output device stop to output the

processed data at an irregular timing after said detector detects the specified pattern;

wherein said printer prints an image on a sheet of paper, based on the

data outputted by said output device; and

The print system according to claim 7, said image processor further comprising a transmitter which transmits a command signal to the printer for feeding a sheet of paper reversely on which an image is formed based on the processed data when the specified pattern is detected by said detector, said printer further comprising a feeder which feeds a sheet of paper reversely in the middle of printing in response to the command signal received from said transmitter.

- 11. (Original) The print system according to claim 10, wherein said printer prints on the sheet which has been fed reversely with a specified color different from that of a document image after feeding the sheet of paper reversely.
 - 12. (Canceled)
- 13. (Currently Amended) A storage medium storing a computerexecutable program comprising the steps of:

processing input data and outputs the processed data; and

detecting whether a specified pattern is included in the input data, in

parallel to the data processing of the input data;

wherein the output of the processed data is stopped at an irregular timing when the specified pattern is detected; and

The storage medium according to claim 12, the program further comprising the step of generating a random timing, wherein the processed data is stopped to be outputted at the random timing after said detector detects the specified pattern.

- 14. (Currently Amended) The storage medium according to claim 12 13, wherein in said processing step the processed data is outputted to a printer.
- 15. (Currently Amended) A method of image processing comprising the steps of:

processing input data and outputting the processed data; and generating a random timing; and

detecting whether a specified pattern is included in the input data or in the processed data, in parallel to the data processing of the input data;

wherein the output of the processed data is stopped at an irregular the generated timing after the specified pattern is detected.